

Fig. 1

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2	1	1	9
1	7	7	7
0	1	3	9
Y:	X: 0	1	2

Fig. 2a

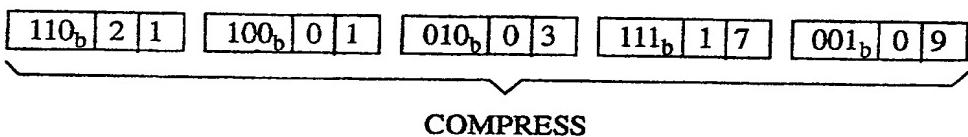


Fig. 2b

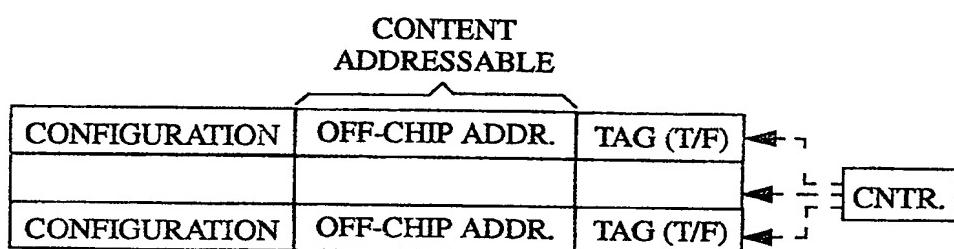


Fig. 3

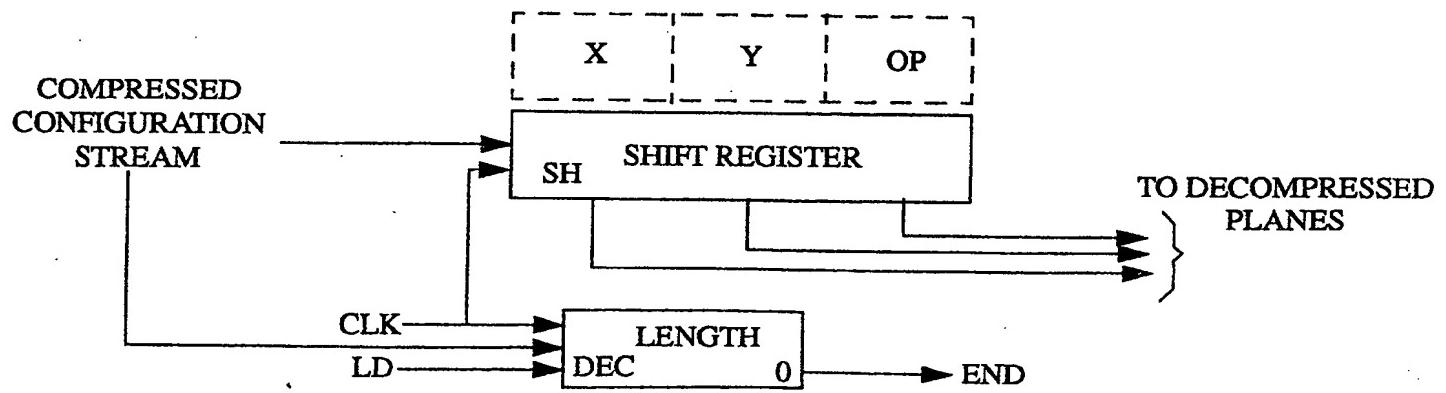


Fig. 4

Blakely, Sokoloff, Taylor & Zafman LLP (503) 684-6200
Title: Hierarchical Storage Architecture for Reconfigurable Logic
Configurations
1st Named Inventor: Christopher E. Phillips
Express Mail No.: EV325527895US Docket No.: 42P16936C.
Sheet: 4 of 8

SCRATCHPAD	
PLANE	CONFIG. ADDR.
0	A ₀
1	A ₁
N	A _N

Fig. 5

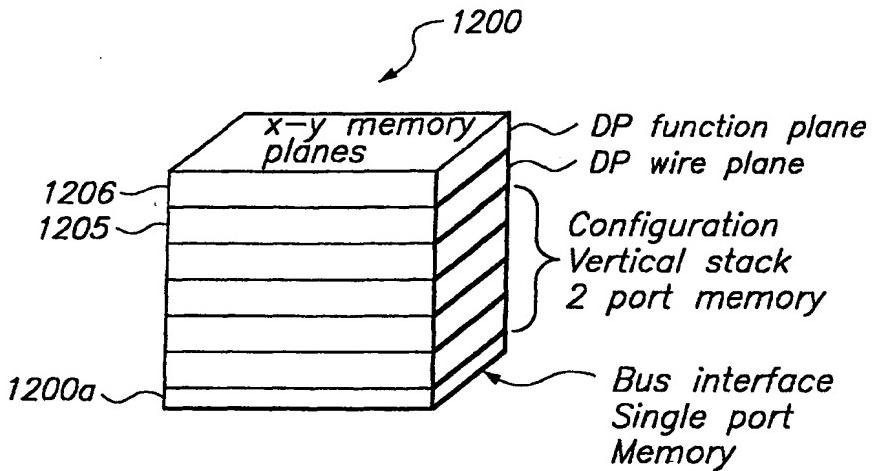


FIG. 6a

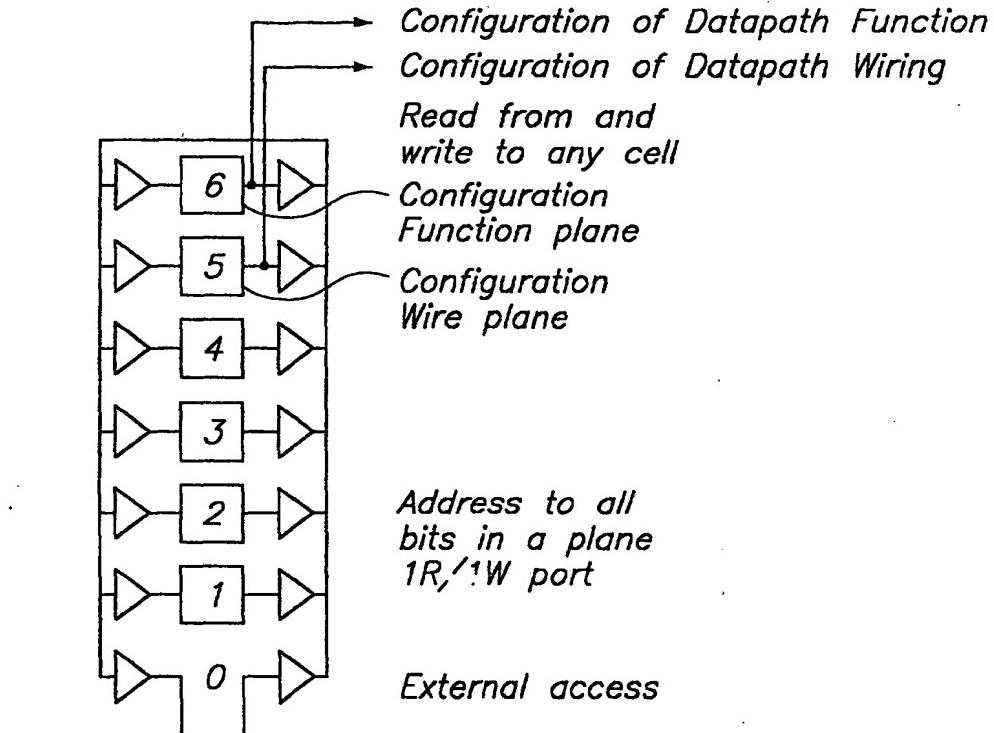


FIG. 6b

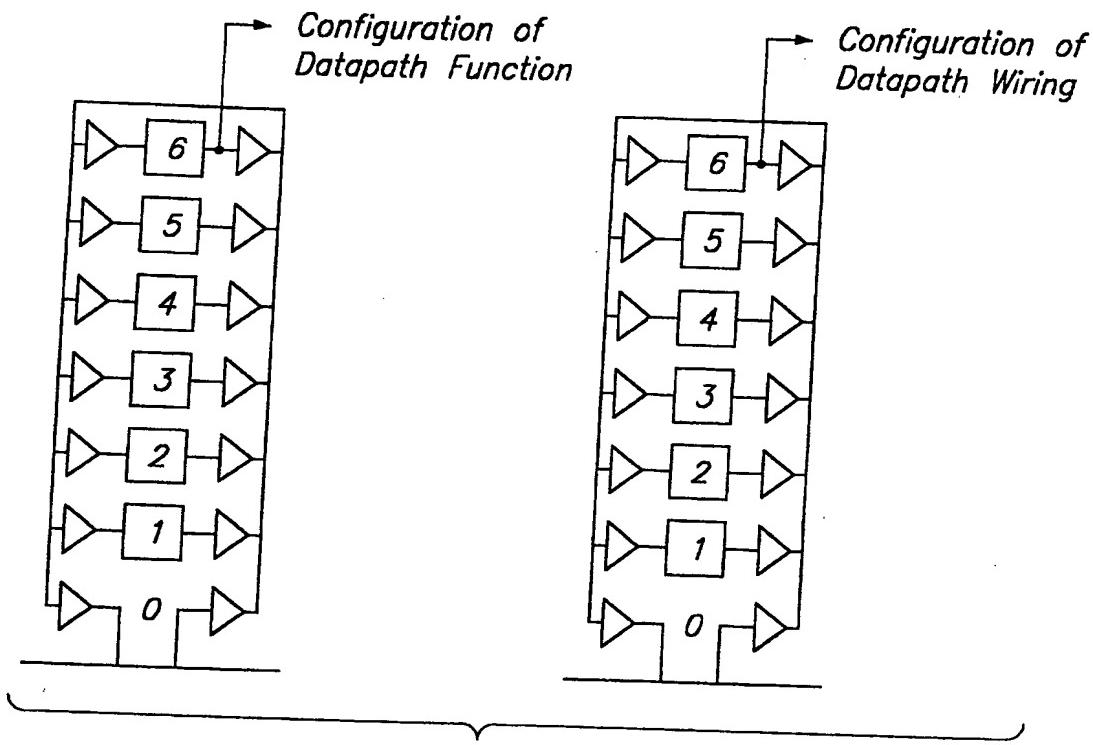


FIG. 6c

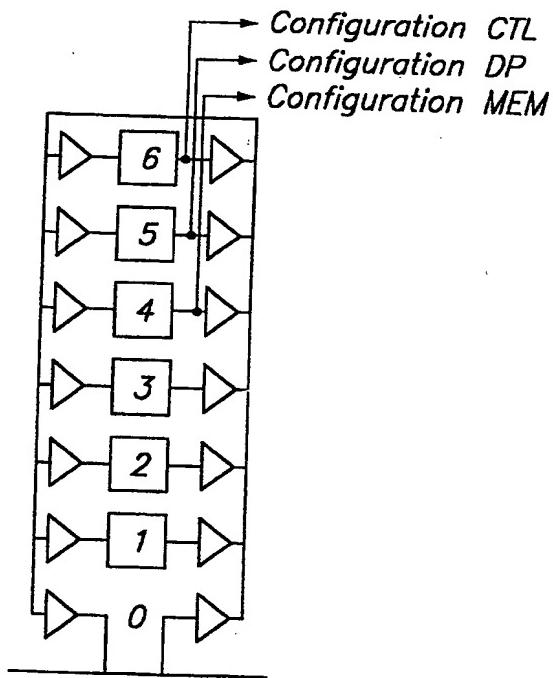


FIG. 6d

005262688 - 1003000

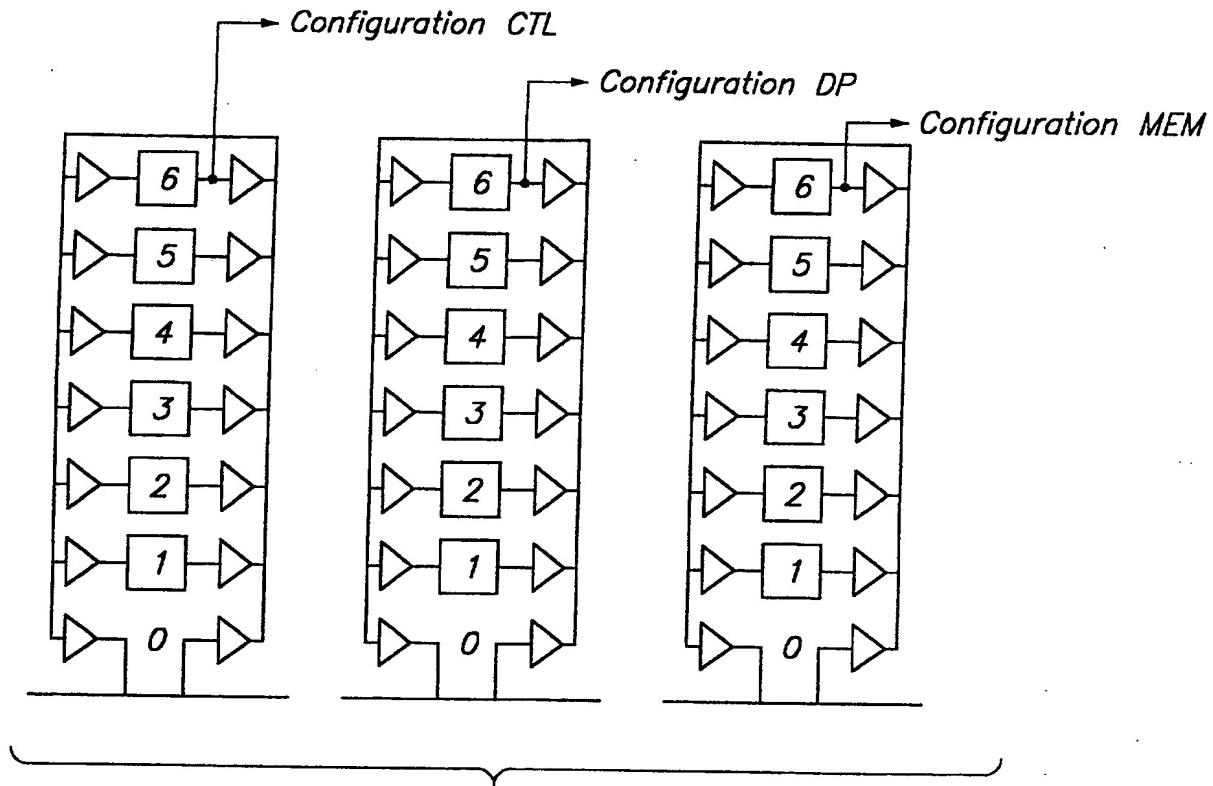
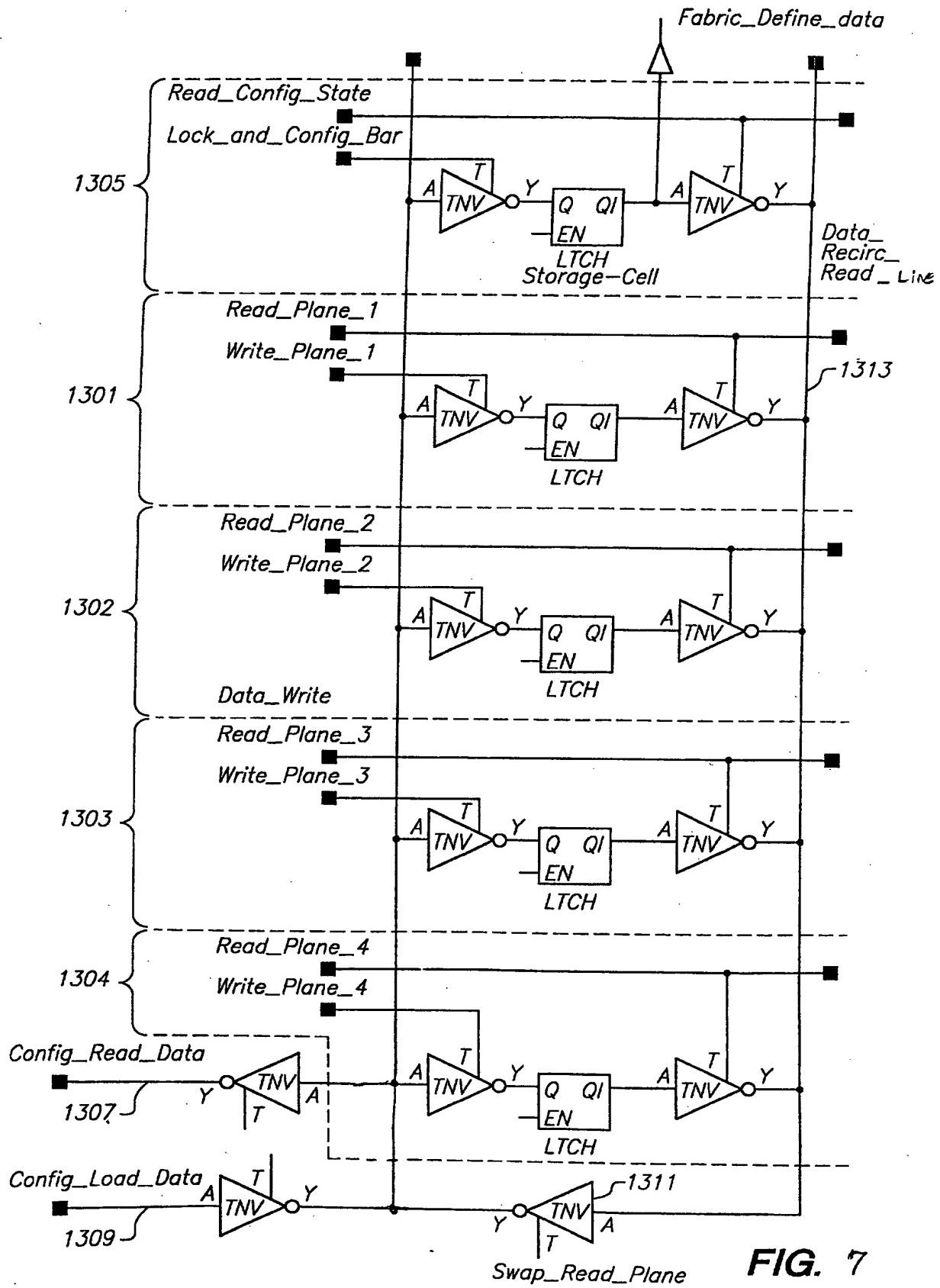


FIG. 6e



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